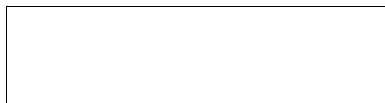


Quadrics Compliance & Performance test record



Test

Location	Quadrics Bristol	Date	2005/08/10
Tested by	Daniel J Blueman		

System under test

Vendor	hp	System	DL145 G2
Processor	1x AMD Opteron 252 (2.6GHz)	Chipset	AMD-8132 (rev none)
BIOS		OS	RedHat EL 4, linux-2.6.9-11.ELsmp
Configuration as tested: 2GB 400MHz DDR			

Quadrics equipment

Product	QM500-B	Part No.	3X-CM500-BA rev A01
Device driver	5.10.5qsnet	Toolchain	gcc-3.4.3-9.EL4, binutils-2.15.92.0.2-10.EL4
qsnet2libs	qsnet2libs-2.2.2-1	qsnetmpi	qsnetmpi-1.24-47.intel81

Basic compliance

Mechanical fit	Intermittent seating issues observed with PCI-X riser
BAR settings	good
MTRR settings	N/A
PCI-X bridge/tunnel settings	12 max outstanding spit transactions
Device driver	patch-free, good

Base PCI-X performance

Max DMA read b/w	884.26 MB/s
Max DMA write b/w	933.21 MB/s
Max PIO write b/w	534.94 MB/s
128 byte PIO write b/w	477.01 MB/s
round-trip base latency	0.87 us

Application performance

Test setup	QM-32A connected with 1m cables on 1 elite
MPI half round trip latency	1.32 us
Put half round trip latency	0.84 us
MPI peak unidirectional BW	883.55 MB/s @ 4 MB
Peak bi-directional BW	878.85 MB/s @ 64 KB

Comments



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Reliability/Soak testing

If run

Description Data transferred Errors logged
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