

## Quadrics Compliance & Performance test record HP RX2600

### Vendor hardware

Vendor	HP	System	RX2600
Processors	Intel 1500MHz Itanium2	Chipset	HP zx1 (rev 23)
BIOS		OS	hp Linux for HPC (RH EL 3)
Memory		Other	

### Quadrics hardware/software

Product	QsNetII 3X-CM500-BA rev A02	Serial	K76A7A1BFPX177
Device driver	4.23qsnet	Tool chain	Gcc 2.96
qsnet2libs	1.8.2-0	qsnetmpi	1.24-39

### Basic compliance

Mechanical fit	No issues
BAR settings	All PCI-X BAR regions setup correctly
MTRR settings	
PCI-X bridge/tunnel settings	
Device driver	No compatibility issues observed

### Base PCI-X performance

Max DMA read b/w	837.21 MB/s @ 8 MB
Max DMA write b/w	991.94 MB/s @ 16 MB
Max PIO write b/w	632.10 MB/s @ 8 KB
128 byte PIO write b/w	609.52 MB/s
Round-trip base latency	1.27 uS

### Application performance

Test setup	256 way QsNetII switch with 136 RX2600s
MPI half round trip latency	2.44 uS
Put half round trip latency	1.54 uS
MPI peak unidirectional BW	836 MB/s @ 4 MB
Peak bi-directional BW	818 MB/s @ 1 MB

### Reliability/Soak testing

Description	Multiple production systems in including PNNL 980xRX2600
Data transferred	
Errors logged	

### Comments

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Signed off on \_\_\_\_\_

By \_\_\_\_\_

### Test

Location	Quadrics, Bristol, UK	Date	8 <sup>th</sup> October 2004
Tested by	Daniel Blueman		