

Quadrics Compliance & Performance test record

HP DL145 G2

Test

Location	ISC2005 Heidelberg	Date	24/06/05
Tested by	Dan Kidger		

System under test

Vendor	HP	System	DL145 G2
Processor	2.6GHz Opteron 252	PCI Chipset	AMD 8132
BIOS	n/k	OS	Rocks 3.3.0
Configuration as tested:			

Quadrics equipment

Product	Elan4 revB	Part No.	K76A7A1500123
Device driver	5.9.5qsnet	Toolchain	unknown
qsnet2libs	2.2.0-12	qsnetmpi	1.24-42

Basic compliance

Mechanical fit	OK
BAR settings	OK
MTRR settings	Only 2GB RAM so no issues
PCI-X bridge/tunnel settings	12 max outstanding split transactions.
Device driver	Patch-free, good

Base PCI-X performance

Max DMA read b/w	884.30 MB/s
Max DMA write b/w	932.25 MB/s
Max PIO write b/w	478.82 MB/s
128 byte PIO write b/w	10.14 MB/s
round-trip base latency	0.86 uSec

Application performance

Test setup	32-way switch, 2m cables, single elite crossing
MPI half round trip latency	1.28-1.31
Put half round trip latency	0.85 uSec
MPI peak unidirectional BW	881.45 MB/s
Peak bi-directional BW	870.81 MB/s

Comments

All went well.

Had a few problems with BIOS not recognising the Elan4 card. Cards were cleaned and reseated (twice). Since then all was stable.

Reliability/Soak testing

If run

Description
Data transferred
Errors logged